

Case No. 02CT08053399CIP1



COMMISSIONER FOR PATENTS  
P.O. BOX 1450  
ALEXANDRIA, VA 22313-1450

In re Application of: **PORTO ET AL.**  
Serial No.: **10/701,150**  
Filed: **November 4, 2003**  
For: **HARDWARE QUANTUM GATE**  
Sir:

Transmitted herewith is an INFORMATION DISCLOSURE STATEMENT in the above-identified application.

1.  This IDS is submitted under 37 C.F.R. § 1.97. No fee is required.
2.  This IDS is submitted under 37 C.F.R. § 1.97(c). Enclosed is a check in the amount of \$180.00.
3.  This IDS is submitted under 37 C.F.R. § 1.97(c) and (e). No fee is required.
4.  This IDS is submitted under 37 C.F.R. § 1.97(d) and (e). Enclosed is a check in the amount of \$130.00 to cover the petition fee.
5.  The Commissioner is hereby authorized to charge or credit any discrepancies in fee amounts to Deposit Account No. 01-0484.
6.  Please associate this application with Customer No. 27975.

 27975

PATENT TRADEMARK OFFICE

Date: November 26, 2003

  
MICHAEL W. TAYLOR  
Reg. No. 43,182

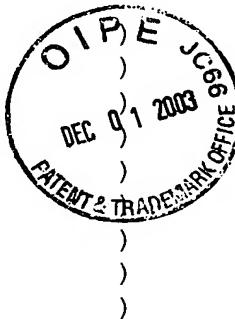
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
PORTO ET AL.

Serial No. 10/701,150

Filing Date: November 4, 2003

For: HARDWARE QUANTUM GATE



CITATION UNDER 37 CFR §1.97

COMMISSIONER FOR PATENTS  
P.O. BOX 1450  
ALEXANDRIA, VA 22313-1450

Sir:

Attached is a form PTO-1449 listing several references for consideration in the examination of the above-identified application. A copy of each reference may be found in parent application, Serial No. 10/615,446 filed July 8, 2003. It is requested that these references be considered by the Examiner and officially made of record in accordance with the provisions of 37 CFR §1.97 and Section 609 of the MPEP.

Respectfully submitted,

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: COMMISSIONER FOR PATENTS, P.O. BOX 1450, ALEXANDRIA, VA 22313-1450, on this 26th day of November, 2003.

*Justin Dyer*

<b>INFORMATION DISCLOSURE STATEMENT</b> 		Atty Docket: 02CT09053399CIP1 Serial No.: 10/701,150 Applicant: PORTO ET AL. Filing Date: November 4, 2003 Group:					
		<b>U.S. PATENT DOCUMENTS</b>					
Examiner Initials		Document Number	Date	Name	Class	Sub Class	Filing Date
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
<b>FOREIGN PATENT DOCUMENTS</b>							
		Document Number	Date	Country	Class	Sub Class	Translation
	AK	1083520	3/14/01	EP	G06N1	00	
	AL	01/67186	9/13/01	WO	G05B12	02	
	AM						
	AN						
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)</b>							
	AO	Hioe et al., A Single Flux Quantum Logic Gate with High Functionality, Superconductor Science and Technology, IOP Publishing, Techno House, Bristol, GB, Vol. 4, No. 11, November 1, 1991, Pages 658-661, XP000561158					
	AP	Miquel et al., Factoring in a Dissipative Quantum Computer, Physical Review, A. General Physics, American Institute of Physics, New York, US, Vol. 54, No. 4, October 4, 1996, pages 2605-2613, XP002181469					
	AQ	Verdal et al., Basic of Quantum Computation, Progress in Quantum Electronics, Pergamon Press, Oxford, GB, Vol. 22, February 1998, Pages 1-39, XP000937713					
	AR						
<b>EXAMINER:</b>				<b>DATE CONSIDERED:</b>			

\***EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.